

IN THE SPECIFICATION

Please amend the first full paragraph beginning on page 11, line 10 to page 12, line 13, as follows:

One purpose of incremental statistical timing is to respond to timing queries after one or more changes is/are made to the electrical circuit being timed or the conditions under which the electrical circuit was timed. To be efficient, retiming the entire circuit is avoided, hence the term "incremental." So the second input to incremental statistical timing is one or more changes, box 110-120, which may be addition of a gate, re-sizing of a gate, addition of a latch, removal of a latch, addition of a wire, removal of a wire, buffering of a wire, etc. Box 120 shows the third input to the system, which is one or more statistical timing queries. Timing queries can include a request for a statistical slack at a timing point, a statistical arrival time or required arrival time at a timing point, a statistical slew (rise/fall time) at a timing point and a request to list the most critical